RECEIVED
CENTRAL FAX CENTER
FEB 25 2008

REMARKS

Present Status of the Application

The Office action mailed on April 23, 2007 and the subsequent Notices of Non-Compliant Amendments dated October 9, 2007, and January 30, 2008, have been carefully considered. In the latest Notice of Non-Compliant Amendment, the Examiner has indicated the erroneously submitted amendment to the specification due to the ambiguous identification of the paragraph to be amended. Said deficiency has been corrected in accordance with 37 C.F.R. 1.121(b)(1)(i) by clearly submitting that the paragraph to be revised is paragraph [0005] instead of [0002]. On the other hand, the amendment to the claims has been again reviewed to ensure the compliance with requirements set forth in 37 C.F.R. 1.121(c)(2).

As for the objections and rejections mentioned in the initial Office action dated April 23, 2007, Applicants respectfully reiterate the detailed action and the arguments hereinafter.

The office Action objected claim 12 because of some required corrections.

The office Action rejected claims 9-19 under 35 U.S.C. 102(b) as being anticipated by Medina et al. (US 6,359,389 B1).

Upon entry of the amendments in this response, claims 1,2 and 4-19 remain pending in the present application. More specifically, claims 1, 2 and 4-8 had been withdrawn, and claims 9, 10, 12, 14 and 16-17 are amended. These amendments are specifically described hereinafter. It is believed that the foregoing amendments add no new matter to the present application.

Response To Objections/Rejections

The sentence "An immense variety of imaging application products is available in market in current days" of paragraph [0002] has been amended to "Immense varieties of imaging application products are available in market in current days".

The phrase "Gamma circuit applied" of claim 12 has been amended to "Gamma circuit is applied".

Response To Claim Rejections Under 35 U.S.C. Section 102

Claims 9-19 are rejected under 35 U.S.C. 102(b) as being anticipated by Medina et al. (US 6,356,389).

Amended claim 9 recites: "[a] programmable Gamma circuit, comprising:

a plurality of Gamma resistors **in parallel**, each of the Gamma resistors having a first terminal and a second terminal, the first terminal of any one of the Gamma resistors receiving one bit signal in digital form of a Gamma setup signal comprised of a plurality of bit signals, and **the second terminal of each of the Gamma resistors being coupled together** where current outputted from each of the Gamma resistors is summed up to a Gamma current; and

an amplifying unit, receiving the Gamma current and outputting a Gamma voltage signal correspondingly."

The amendment of claim 9 "a plurality of Gamma resistors communicated in parallel" can be supported by FIG. 4. Therefore, there is no new matter added.

Examiner states Medina et al. discloses the gamma controlling voltage circuit 240b (defined as the Gamma circuit by Examiner) includes the programmable potentiometers 514 and 524 (defined as the Gamma resistor by Examiner) and, the operational amplifier 520 (defined as the amplifying unit by Examiner).

However, in Medina et al., the programmable potentiometers 514 and 524 are not communicated in parallel, and they have no any relation (See Figure 5). Furthermore, the programmable potentiometers 514 and 524 have no second thermals coupled together. Therefore, Medina et al. fails to teach or suggest "a plurality of Gamma resistors **communicated in parallel**" and "**the second terminal of each of the Gamma resistors being coupled together**" as recited in amended claim 9.

For at least the foregoing reasons, applicant respectfully submits that Medina et al. do not teach each and every element in amended claim 9. Independent claim 9 patently defines over the prior art reference, and should be allowed.

Further, amended claim 10 recites: "[T]he programmable Gamma circuit as recited in claim 9, wherein the amplifying unit comprises:

a feedback resistor, having a third terminal and a fourth terminal; and

an operational amplifier, having a first input terminal, a second input terminal and an output terminal, wherein the first input terminal is coupled to a voltage level, the second input terminal is coupled to the third terminal of the feedback resistor and is coupled to the second terminal of each of the Gamma resistors, which are coupled together, to receives receive the Gamma current, **the amount of the bit signals being**

equal to the amount of the Gamma resistors, each of the Gamma resistors comprising a resistance different from others; and the output terminal and the fourth terminal of the feedback resistor are coupled and output the Gamma voltage signal."

The additions of claim 10 "the amount of the bit signals being equal to the amount of the Gamma resistors" and "each of the Gamma resistors having a resistance different from others" can be supported by paragraph [0023] of the original specification. Therefore, there is no new matter added. Medina et al. fails to teach or suggest "the amount of the bit signals being equal to the amount of the Gamma resistors" and "each of the Gamma resistors having a resistance different from others" as recited in amended claim 10. Therefore, dependent claim 10 patently defines over the prior art reference, and should be allowed.

Claims 11-13 depend on claim 9, therefore, claims 11-13 should be allowed.

Claims 14 is similar to claim 9, for the above reasons, claim 14 is patentable over Medina et al.

Claims 15-19 depend on claim 14, therefore, claims 15-19 should be allowed.

RECEIVED
CENTRAL FAX CENTER
FEB 25 2008

Customer No.: 31561
Docket No.: 12036-US-PA
Application No.: 10/708,365

CONCLUSION

In view of the foregoing reasons, it is believed that the pending claims 9-19 are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

Respectfully submitted,

Date : Feb. 22, 2008

Belinda Lee

Belinda Lee
Registration No.: 46,863

Jianq Chyun Intellectual Property Office
7th Floor-1, No. 100
Roosevelt Road, Section 2
Taipei, 100
Taiwan
Tel: 011-886-2-2369-2800
Fax: 011-886-2-2369-7233
Email: belinda@jcipgroup.com.tw
Usa@jcipgroup.com.tw